

S-Band Radar Transistor

The high power pulsed radar transistor device part number IB2226MH160 is designed for S-Band radar systems operating over the instantaneous bandwidth of 2.25-2.55 GHz. While operating in class C mode this common base device supplies a minimum of 160 watts of peak pulse power under the conditions of 200 μ s pulse width and 10% duty cycle. All devices are 100% screened for large signal RF parameters, including power gain compression. This device is rated for a peak output power level of $P_{PEAK} = 160W$ @ 10% duty factor. This corresponds to an average power $P_{AVG} = 16W$.



TYPICAL DATA TYPICAL DATA TYPICAL DATA TYPICAL DATA

FREQ (GHz)	PW (us)	DUTY (%)	VCC (V)	P IN (W)	IRL (dB)	POUT (W)	GP (dB)	OPF (dB)	IC (A)	NC (%)	DROOP (dB)
2.25	200	10.0	34.0	24.0	-20.3	182.1	8.8	0.34	10.73	49.9	-0.20
2.40	200	10.0	34.0	24.0	-15.4	188.1	9.0	-	12.14	45.5	-0.42
2.55	200	10.0	34.0	24.0	-19.7	173.8	8.6	-	11.06	46.2	-0.25

Integra

TECHNOLOGIES, INC.

Silicon Bipolar

- Ultra-high f_T

Class C Operation

- High Efficiency

Common Base Configuration

- Single Power Supply

Gold Metal

- Maximum Reliability

Emitter Ballasting

- Optimum Thermal Distribution

Internal Impedance Matching

- Ease of Use
- Ultra-low Loss Design

BeO Package

- Solder Seal Hermeticity
- Unmatched Thermal Reliability

RF Test Fixture

- Broadband
- Matched to 50 Ω
- Long-term Correlation
- 100% Device RF Screening
- No External Tuning Allowed
- Micro-strip structure on soft pc board with dielectric constant 10.2

US Patent Number

- US 8344809B2

MAXIMUM RATINGS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
BD	Collector-Emitter Voltage	V_{CES}	--	70	V	$V_{BE}=0V$
BD	Storage Temperature Range	T_{STG}	-65	+200	°C	--
BD	Operating Junction Temperature Range	T_J	-55	+200	°C	--
BD	CW Operation	--	--	--	--	Not rated for CW operation.
Note Screen 'BD' = parameter qualified By Design.						

THERMAL CHARACTERISTICS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
BD	Thermal Resistance	$R_{TH(JC)}$	--	0.39	°C/W	$V_{CC}=V1$, $PW=PW1$, $DF=DF1$, $T_F=25\pm5^\circ C$, $P_{OUT}=160W$.
Note Screen 'BD' = parameter qualified By Design.						

PROCESSING SPECIFICATIONS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	DC Wafer Probe	--	--	--	--	Per Integra specification.
Q1	Wafer DC and RF Qualification	--	--	--	--	Per Integra specification.
LM	Wire Bond Strength	--	--	--	--	Line monitor per Integra specification.
100%	Pre-cap visual inspection	--	--	--	--	Per Integra specification
100%	Gross leak test	--	--	--	--	MIL-STD-750D, Method 1071.6, Test Condition C
Note Screen 'Q1' = parameter is qualified by assembly and test of 3 pieces minimum per wafer.						
Note Screen 'LM' = parameter is qualified by assembly line monitor.						

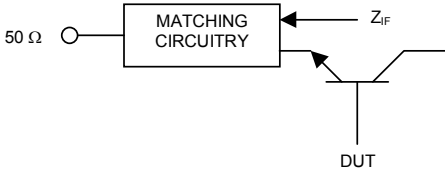
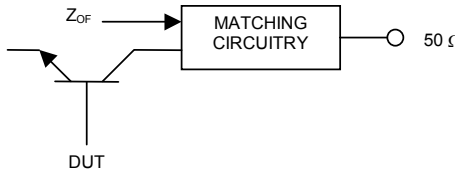
DC ELECTRICAL CHARACTERISTICS

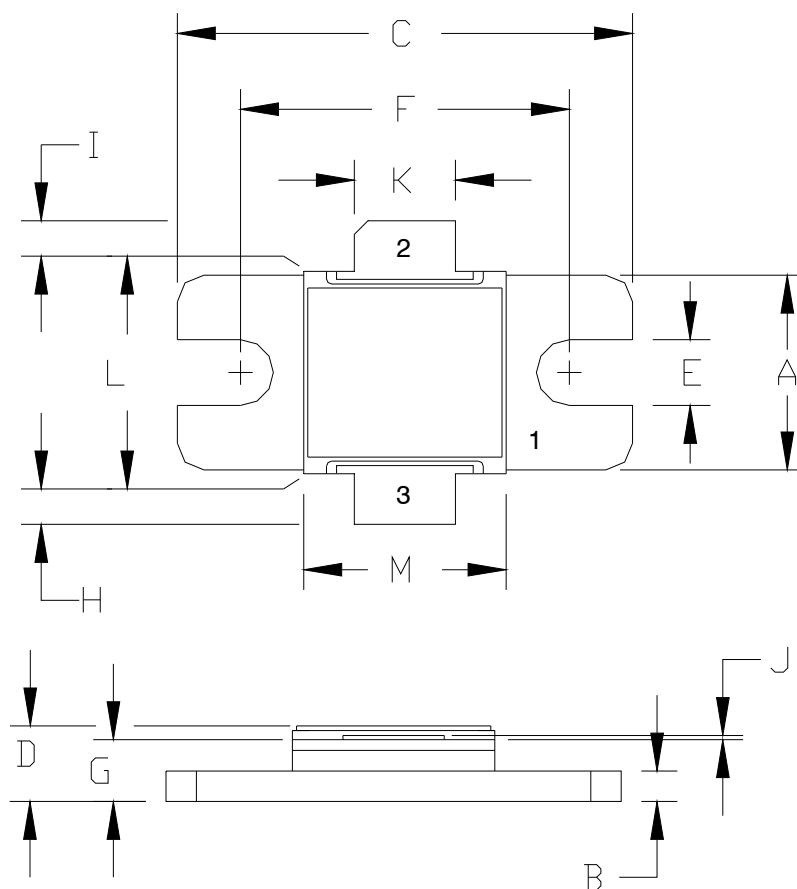
Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	Collector-Emitter Breakdown Voltage	BV_{CES}	65	--	V	$I_C = 30mA$, $V_{BE} = 0V$, $T_F = 25\pm5^\circ C$.
100%	Zero Base Voltage Collector Leakage Current	I_{CES}	--	6	mA	$V_{CE} = 30V$, $V_{BE} = 0V$, $T_F = 25\pm5^\circ C$.
100%	DC Current Gain	H_{FE}	10	120	--	$V_{CE} = 5V$, $I_C = 100mA$, $T_F = 25\pm5^\circ C$.

RF ELECTRICAL CHARACTERISTICS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	Input Return Loss	IRL	-18	-9	dB	$V_{CC}=V1$, $PW=PW1$, $DF=DF1$, $T_F=T_{F1}$, $P_{IN}=P_{IN1}$, P_{IN2} , P_{IN3} , $F=F1$, $F2$, $F3$.
100%	Output Power	P_O	160	220	W	$V_{CC}=V1$, $PW=PW1$, $DF=DF1$, $T_F=T_{F1}$, $P_{IN}=P_{IN1}$, P_{IN2} , P_{IN3} , $F=F1$, $F2$, $F3$.
100%	Output Power Flatness = $10 \cdot \text{LOG}(P_{O-MAX}/P_{O-MIN})$	OPF	--	1.0	dB	$V_{CC}=V1$, $PW=PW1$, $DF=DF1$, $T_F=T_{F1}$, $P_{IN}=P_{IN1}$, P_{IN2} , P_{IN3} , $F=F1$, $F2$, $F3$.
100%	Output Power Compression = $10 \cdot \text{LOG}(P_{O-C}/P_{O-NOM})$	OPC	+0.15	+1.10	dB	$V_{CC}=V1$, $PW=PW1$, $DF=DF1$, $T_F=T_{F1}$, $P_{IN}=P_{IN1}+1\text{dB}$, $P_{IN2}+1\text{dB}$, $P_{IN3}+1\text{dB}$, $F=F1$, $F2$, $F3$.
100%	Collector Efficiency ($P_O/I_C/V_{CC}$)	N_C	43	--	%	$V_{CC}=V1$, $PW=PW1$, $DF=DF1$, $T_F=T_{F1}$, $P_{IN}=P_{IN1}$, P_{IN2} , P_{IN3} , $F=F1$, $F2$, $F3$.
100%	Pulse Amplitude Droop	D	-0.6	+0.3	dB	$V_{CC}=V1$, $PW=PW1$, $DF=DF1$, $T_F=T_{F1}$, $P_{IN}=P_{IN1}$, P_{IN2} , P_{IN3} , $F=F1$, $F2$, $F3$.
100%	Stability into 2:1 VSWR	VSWR-S	--	--	--	$V_{CC}=V1$, $PW=PW1$, $DF=DF1$, $T_F=T_{F1}$, $P_{IN}=P_{IN1}$, P_{IN2} , P_{IN3} , $F=F1$, $F2$, $F3$. Rotate 2:1 output VSWR through 360° phase. No oscillatory or pulse break-up characteristics allowed on detected output pulse. All non-harmonically related signals must be at least -50 dBc.
100%	3:1 Load Mismatch Tolerance	LMT	--	--	--	$V_{CC}=V1$, $PW=PW1$, $DF=DF1$, $T_F=T_{F1}$, $P_{IN}=P_{IN1}$, P_{IN2} , P_{IN3} , $F=F1$, $F2$, $F3$. Rotate 3:1 output VSWR through 360° phase. Post test P_O = Pre test $P_O \pm 0.2\text{dB}$.
100%	Delta Insertion Phase Variation	d-IP	-30	+30	Deg	$V_{CC}=V1$, $PW=PW1$, $DF=DF1$, $T_F=T_{F1}$, $P_{IN}=P_{IN2}$, $F=F2$, Mark in 5° increments. Measure at $T=PW1 \pm 2$ time position.
BD	Pulse Risetime	RT	--	150	ns	$V_{CC}=V1$, $PW=PW1$, $DF=DF1$, $T_F=T_{F1}$, $P_{IN}=P_{IN1}$, P_{IN2} , P_{IN3} , $F=F1$, $F2$, $F3$. Measure between 10% and 90% detected power points.
Note	$V1 = 34V \pm 0.2V$; $PW1 = 200\mu\text{s}$; $DF1 = 10\%$; $P_{IN1} = P_{IN2} = P_{IN3} = 24W$; $F1 = 2.25\text{ GHz}$, $F2 = 2.40\text{ GHz}$, $F3 = 2.55\text{ GHz}$, $T_{F1} = 25 \pm 5^\circ\text{C}$.					
Note	T_F = Device flange temperature.					
Note	Screen 'BD' = parameter qualified By Design.					

RF TEST FIXTURE IMPEDANCE CHARACTERISTICS

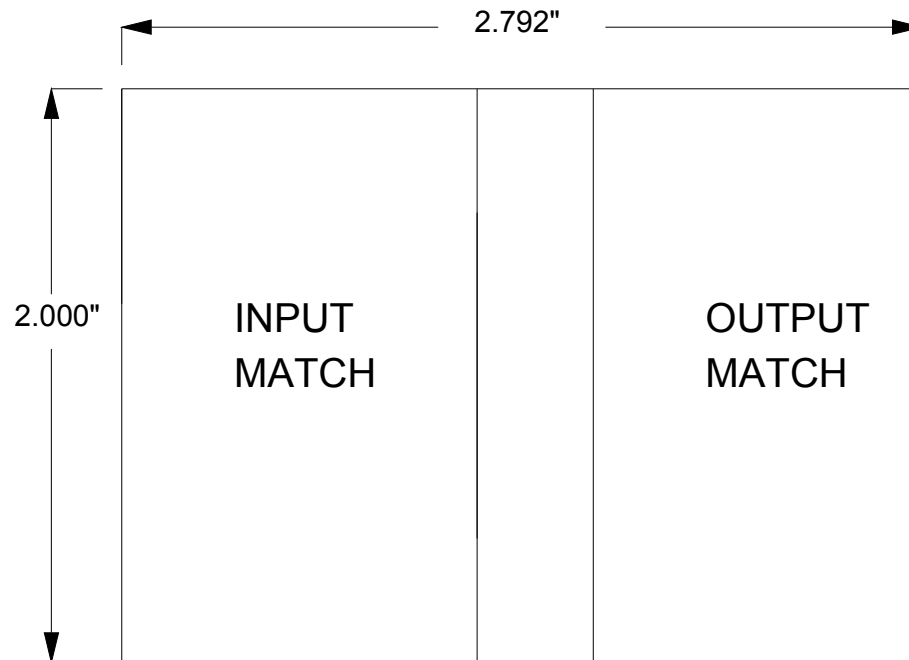
Frequency (MHz)	Z_{IF} (Ω)	Z_{OF} (Ω)
2.25	2.6 - j5.8	2.5 - j4.3
2.40	2.4 - j5.3	2.2 - j4.0
2.55	2.2 - j4.7	1.6 - j3.5
Impedance Definition		

PACKAGE DIMENSIONAL OUTLINE DRAWING


DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.380	0.390	9.65	9.91
B	0.055	0.065	1.40	1.65
C	0.895	0.905	22.73	22.99
D	0.150	0.180	3.81	4.57
E	0.125	0.135	3.18	3.43
F	0.645	0.655	16.38	16.64
G	0.110	0.130	2.79	3.30
H	0.125	0.129	3.17	3.28
I	0.125	0.129	3.17	3.28
J	0.003	0.005	0.08	0.13
K	0.195	0.205	4.95	5.21
L	0.395	0.405	10.03	10.29
M	0.395	0.405	10.03	10.29

PIN	
1	BASE
2	COLLECTOR
3	EMITTER

RF TEST FIXTURE



ASSEMBLY AND PARTS LIST

CONTACT FACTORY FOR RF TEST FIXTURE CAD DRAWING WITH CIRCUIT DIMENSIONS AND COMPONENT LIST

DEFINITIONS

Data Sheet Status	
Proposed Specification	This data sheet contains proposed specifications.
Preliminary Specification	This data sheet contains specifications based on preliminary measurements and data.
Product Specification	This data sheet contains final product specifications.
Maximum Ratings	
Stress above one or more of the maximum ratings may cause permanent damage to the device. These are maximum ratings only. Operation of the device at these or at any other conditions above those given in the characteristics sections of the specification is not implied. Exposure to maximum values for extended periods of time may affect device reliability.	

WARNING

Product and environmental safety - toxic materials
This product contains beryllium oxide. The product is entirely safe provided that the BeO base is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with general or domestic waste.

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